

### **AMENDMENTS TO THE CLAIMS**

Please amend the claims as follows:

#### **Listing of Claims:**

Claim 1 (Currently Amended): A spin-tunnel transistor, comprising:

an emitter;

a collector formed adjacent to the emitter;

a base formed between the emitter and the collector and having a magnetization pinned layer of ferromagnetic material, a magnetization free layer of ferromagnetic material and a nonmagnetic layer between the magnetization pinned layer of ferromagnetic material and the magnetization free layer of ferromagnetic material, the magnetization pinned layer having a magnetization substantially fixed in an applied magnetic field, the magnetization free layer having a magnetization substantially free to rotate under the applied magnetic field, and the nonmagnetic layer being configured to decouple ~~decouples~~ exchange coupling between the magnetization free layer of ferromagnetic material and the magnetization pinned layer of ferromagnetic material; and

a tunnel barrier layer of antiferromagnetic material formed between the magnetization pinned layer of ferromagnetic material and the emitter or between the collector and the magnetization pinned layer of ferromagnetic material and provided with an exchange coupling with adjoining one of the magnetization pinned layer of ferromagnetic material, the magnetization of the magnetization pinned layer of ferromagnetic material being fixed by the exchange coupling between the magnetization pinned layer of ferromagnetic material and the tunnel barrier of antiferromagnetic material.

Claim 2 (Original): A spin-tunnel transistor of claim 1, wherein the antiferromagnetic material is cobalt oxide, iron oxide or nickel oxide.

Claim 3 (Original): A spin-tunnel transistor of claim 1, further comprising a dielectric layer of nonmagnetic material formed in contact with the tunnel barrier layer of antiferromagnetic material.

Claim 4 (Currently Amended): A spin-tunnel transistor of claim 1, wherein the magnetization pinned layer includes ~~contains~~ a metal selected from the group consisting of Fe, Co, Ni, or an alloy containing the metal, and the tunnel barrier layer of antiferromagnetic material contains an oxide of the metal.

Claim 5 (Currently Amended): A spin-tunnel transistor of claim 1, wherein the tunnel barrier layer of antiferromagnetic material is formed between the magnetization pinned layer and the emitter, and the emitter includes ~~has~~ a semiconductor surface contacting the tunnel barrier layer of antiferromagnetic material.

Claim 6 (Original): A spin-tunnel transistor of claim 1, further comprising another tunnel barrier layer of nonmagnetic dielectric material formed in contact with the magnetization free layer of ferromagnetic material.

Claim 7 (Original): A magnetic reproducing head, comprising:  
a spin-tunnel transistor comprising:  
an emitter;  
a collector formed adjacent to the emitter;  
a base formed between the emitter and the collector and having a magnetization pinned layer of ferromagnetic material, a magnetization free layer of ferromagnetic material and a nonmagnetic layer between the magnetization pinned layer of ferromagnetic material

and the magnetization free layer of ferromagnetic material, the magnetization pinned layer having a magnetization substantially fixed in an applied magnetic field, and the magnetization free layer having a magnetization substantially free to rotate under the applied magnetic field; and

a tunnel barrier layer of antiferromagnetic material formed between the magnetization pinned layer of ferromagnetic material and the emitter or between the collector and the magnetization pinned layer of ferromagnetic material and provided with an exchange coupling with adjoining one of the magnetization pinned layer of ferromagnetic material, the magnetization of the magnetization pinned layer of ferromagnetic material being fixed by the exchange coupling between the magnetization pinned layer of ferromagnetic material and the tunnel barrier of antiferromagnetic material.

Claim 8 (Original): A magnetic reproducing head of claim 7, wherein the collector is electrically coupled with an electrical field effect transistor, and the spin-tunnel transistor and the electrical field effect transistor are formed on a same substrate.

Claim 9 (Original): A magnetic reproducing head of claim 8, further comprising a magnetic flux guide magnetically coupled with the magnetization free layer.

Claim 10 (New): A magnetic reproducing head, comprising:  
a spin-tunnel transistor comprising:  
an emitter;  
a collector formed adjacent to the emitter;  
a base formed between the emitter and the collector and having a magnetization pinned layer of ferromagnetic material, a magnetization free layer of ferromagnetic material

and a nonmagnetic layer between the magnetization pinned layer of ferromagnetic material and the magnetization free layer of ferromagnetic material, the magnetization pinned layer having a magnetization substantially fixed in an applied magnetic field, the magnetization free layer having a magnetization substantially free to rotate under the applied magnetic field; and, the nonmagnetic layer being configured to decouple exchange coupling between the magnetization free layer of ferromagnetic material and the magnetization pinned layer of ferromagnetic material; and

a tunnel barrier layer of antiferromagnetic material formed between the magnetization pinned layer of ferromagnetic material and the emitter or between the collector and the magnetization pinned layer of ferromagnetic material and provided with an exchange coupling with adjoining one of the magnetization pinned layer of ferromagnetic material, the magnetization of the magnetization pinned layer of ferromagnetic material being fixed by the exchange coupling between the magnetization pinned layer of ferromagnetic material and the tunnel barrier of antiferromagnetic material.

Claim 11 (New): The magnetic reproducing head of claim 10, wherein the collector is electrically coupled with an electrical field effect transistor, and the spin-tunnel transistor and the electrical field effect transistor are formed on a same substrate.

Claim 12 (New): The magnetic reproducing head of claim 10, further comprising a magnetic flux guide magnetically coupled with the magnetization free layer.